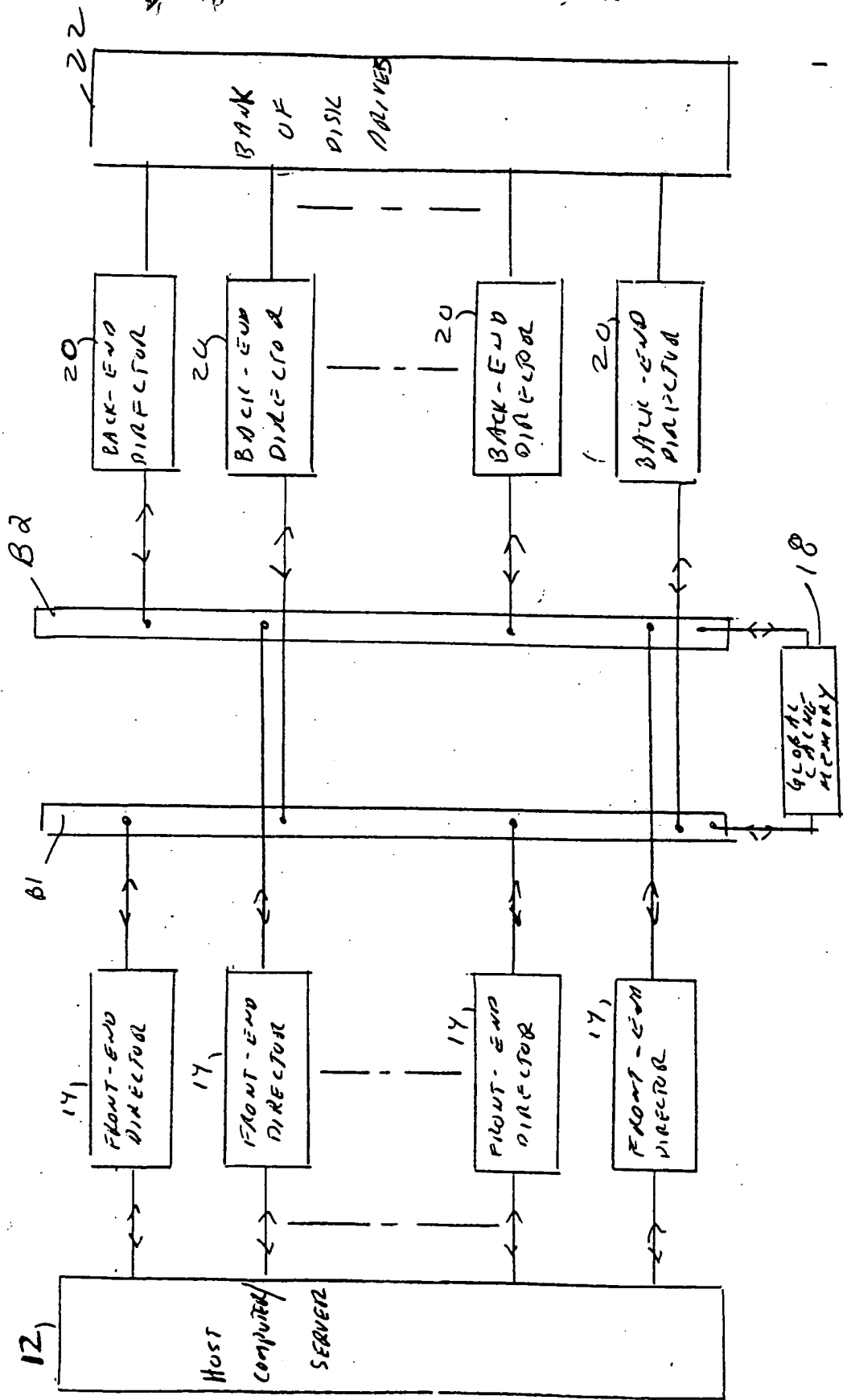


FIG. 1
DATA NET



U.S. PAT. OFF. 1994

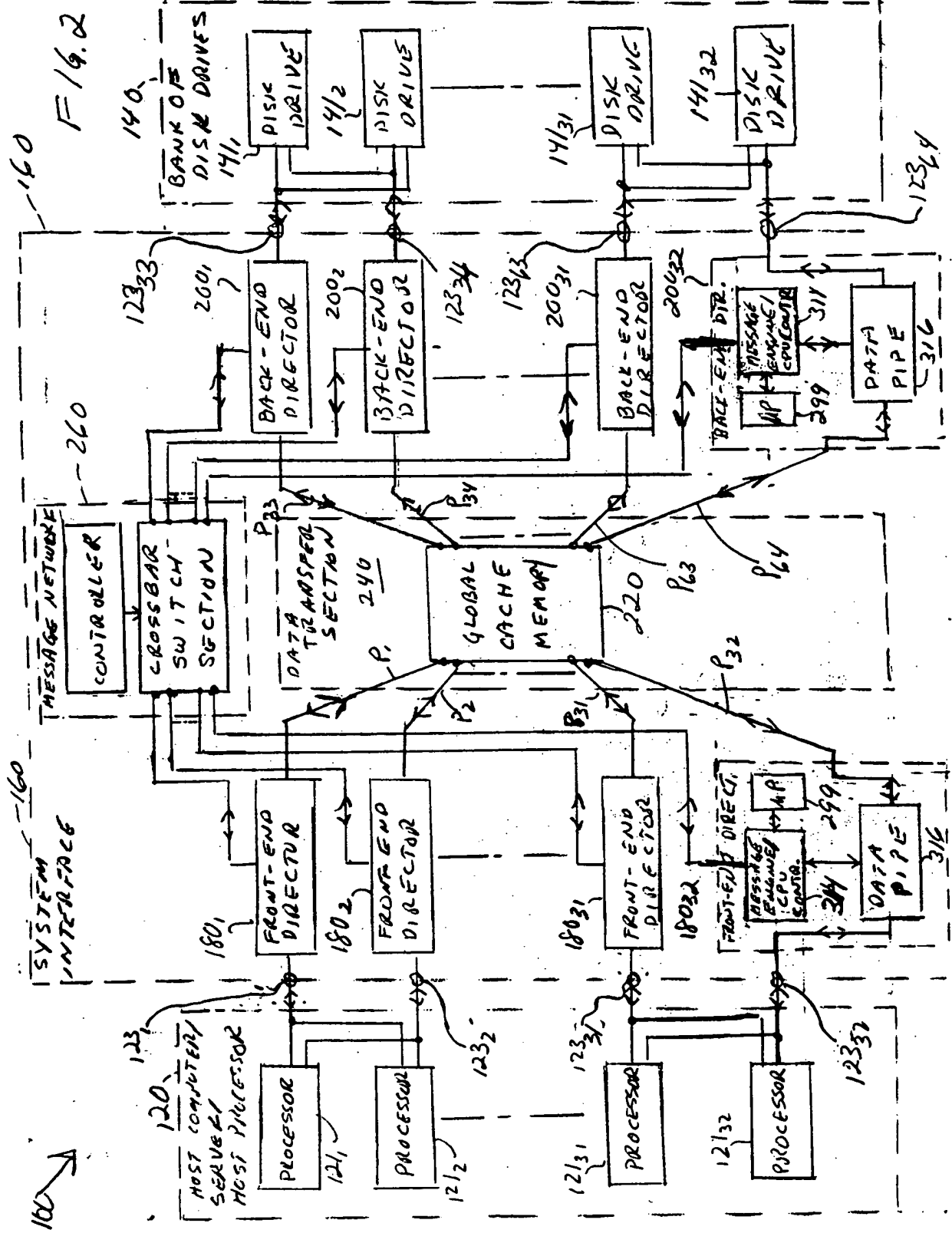


FIG. 3

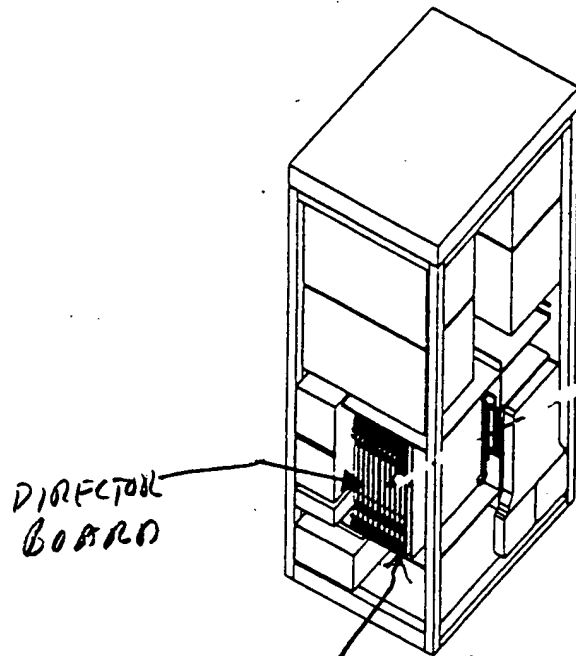
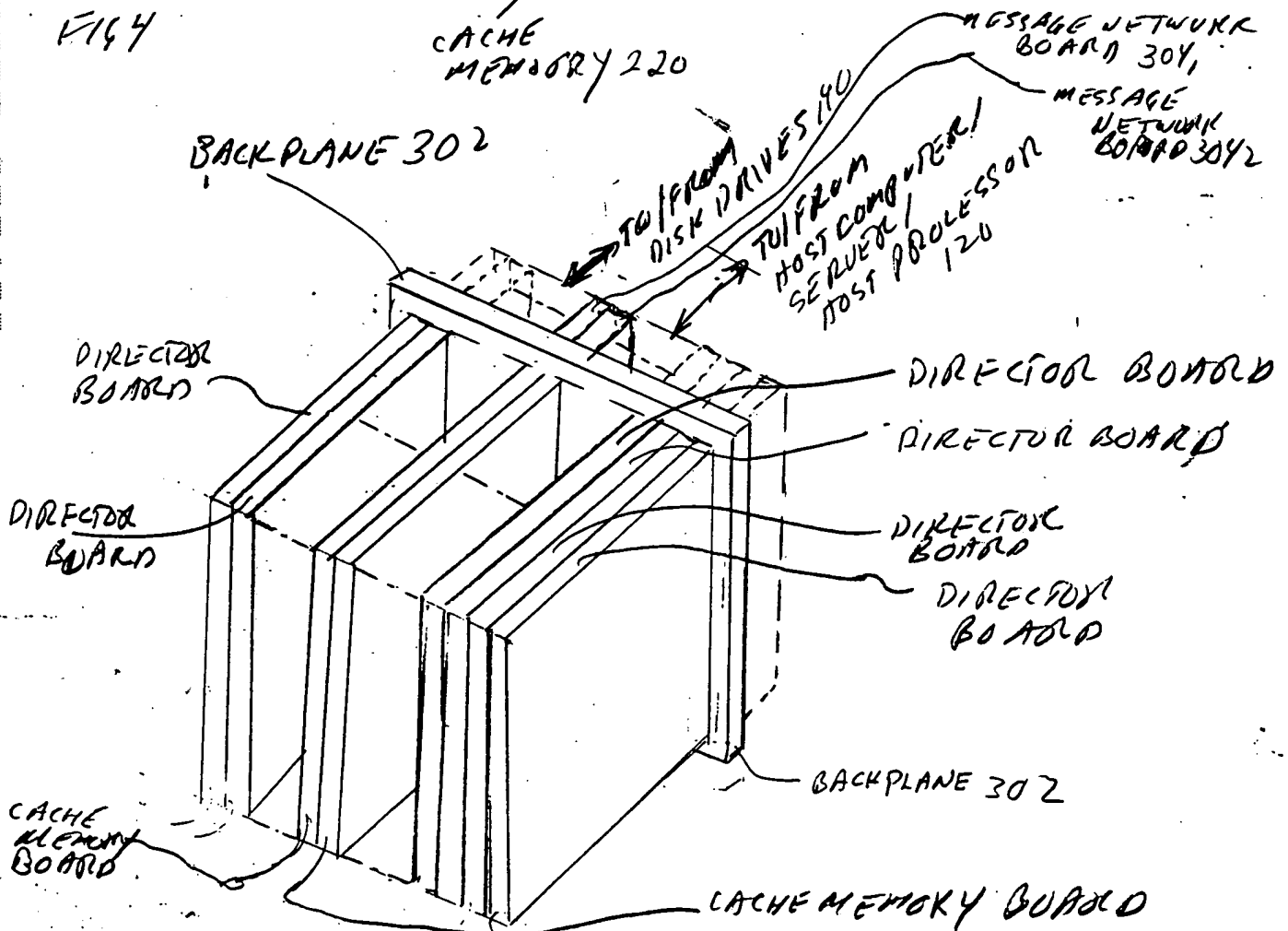
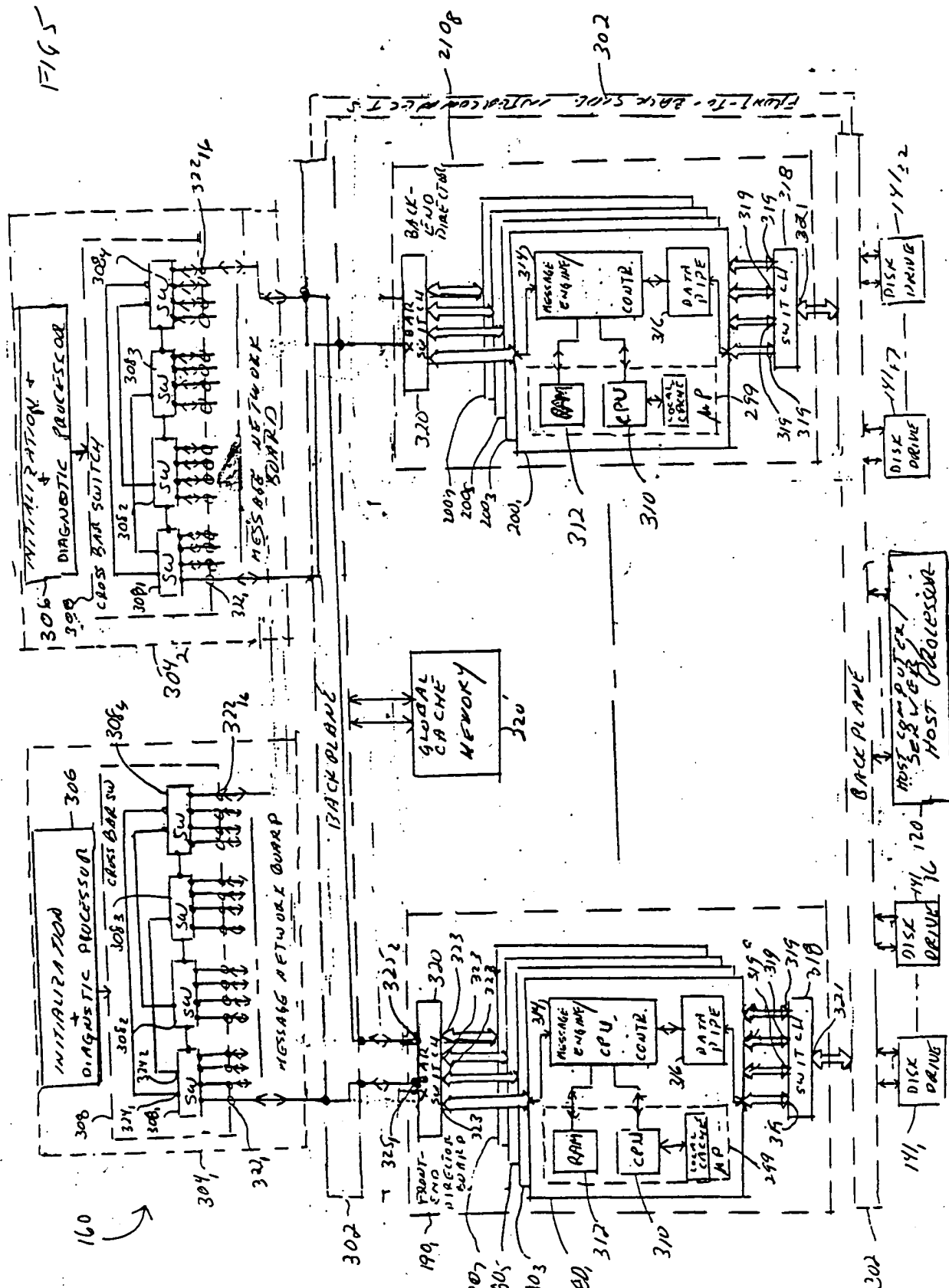


FIG. 4



5911



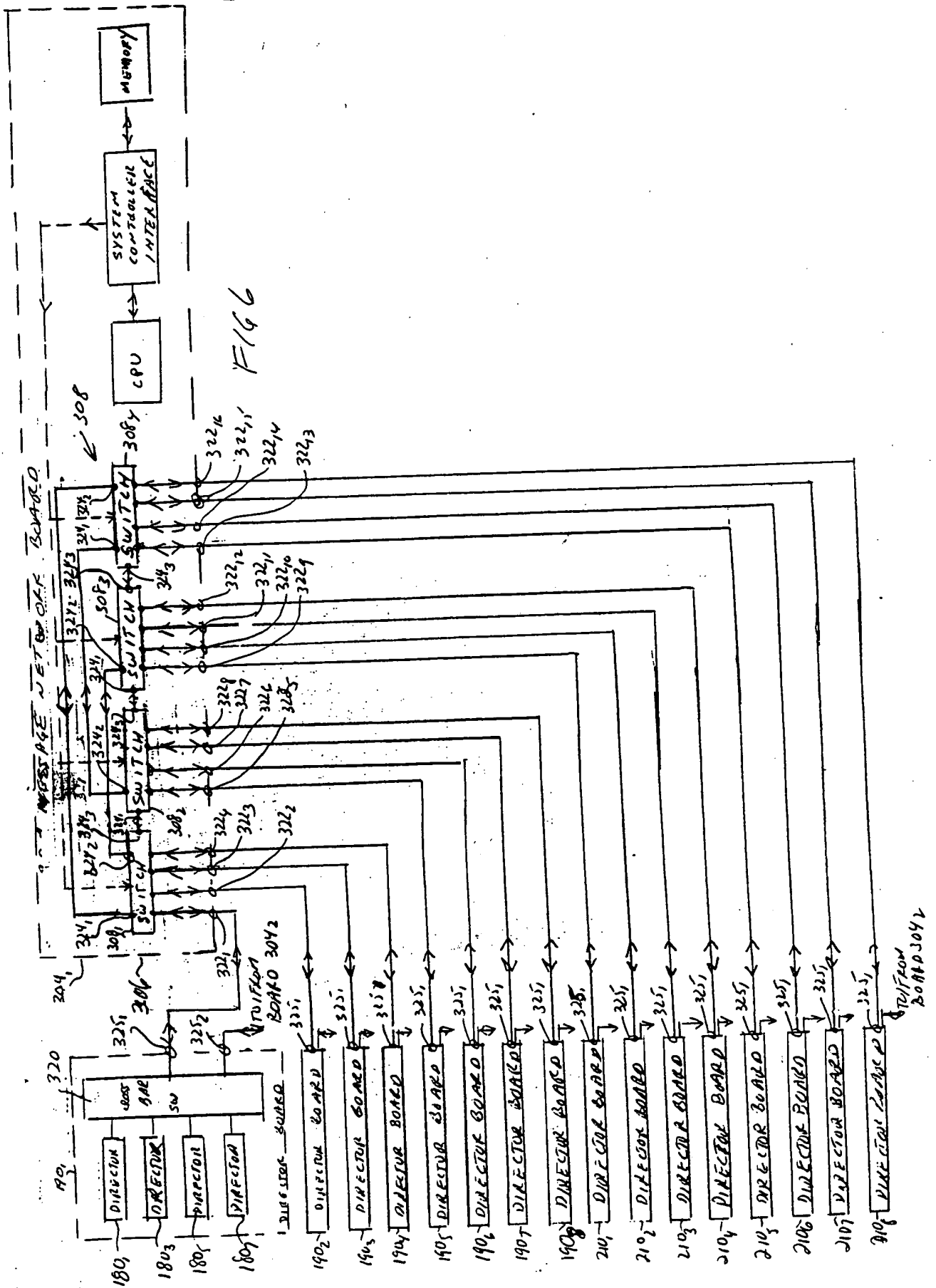
[illegible]

FIG. 8A

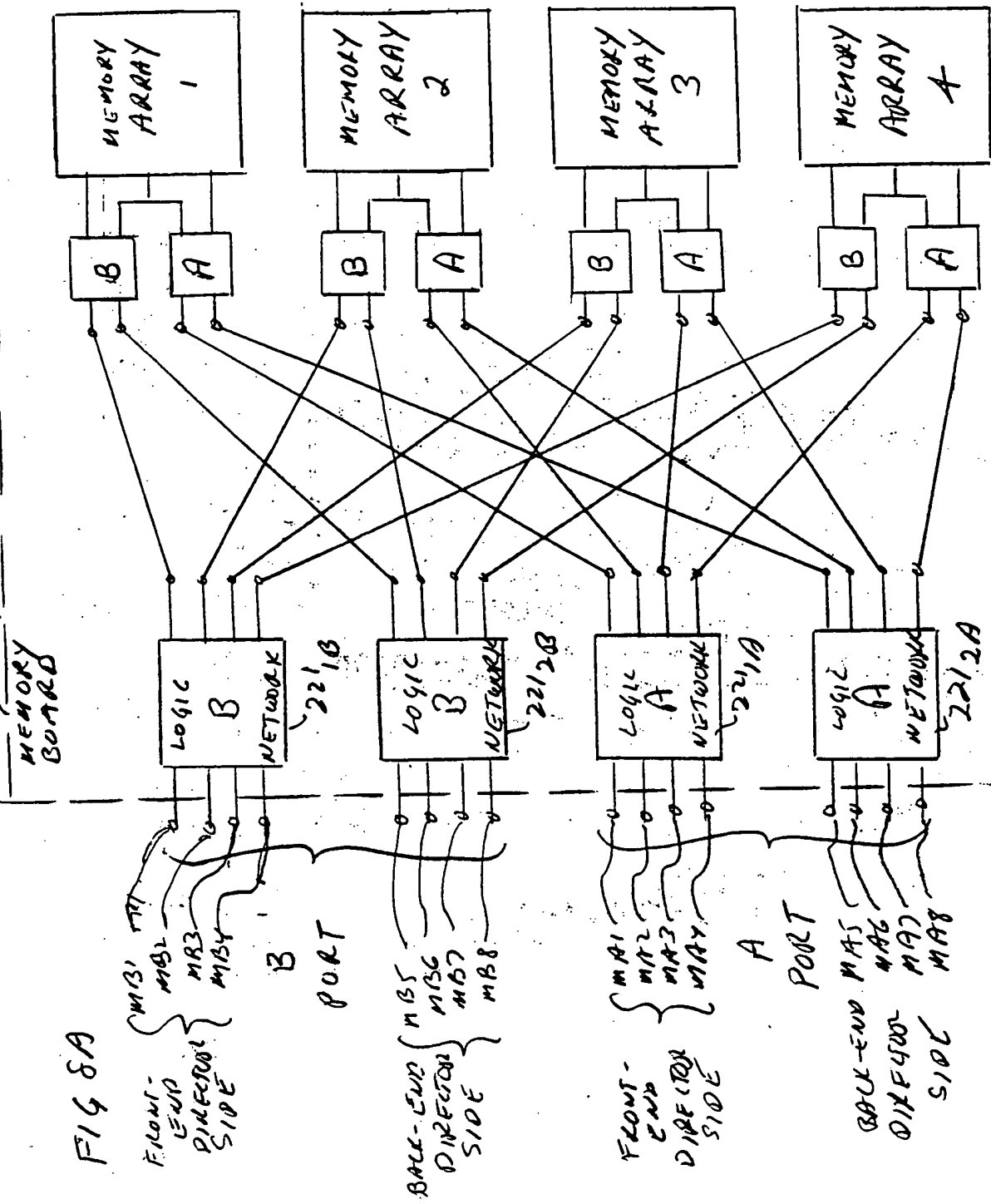


FIG 8B

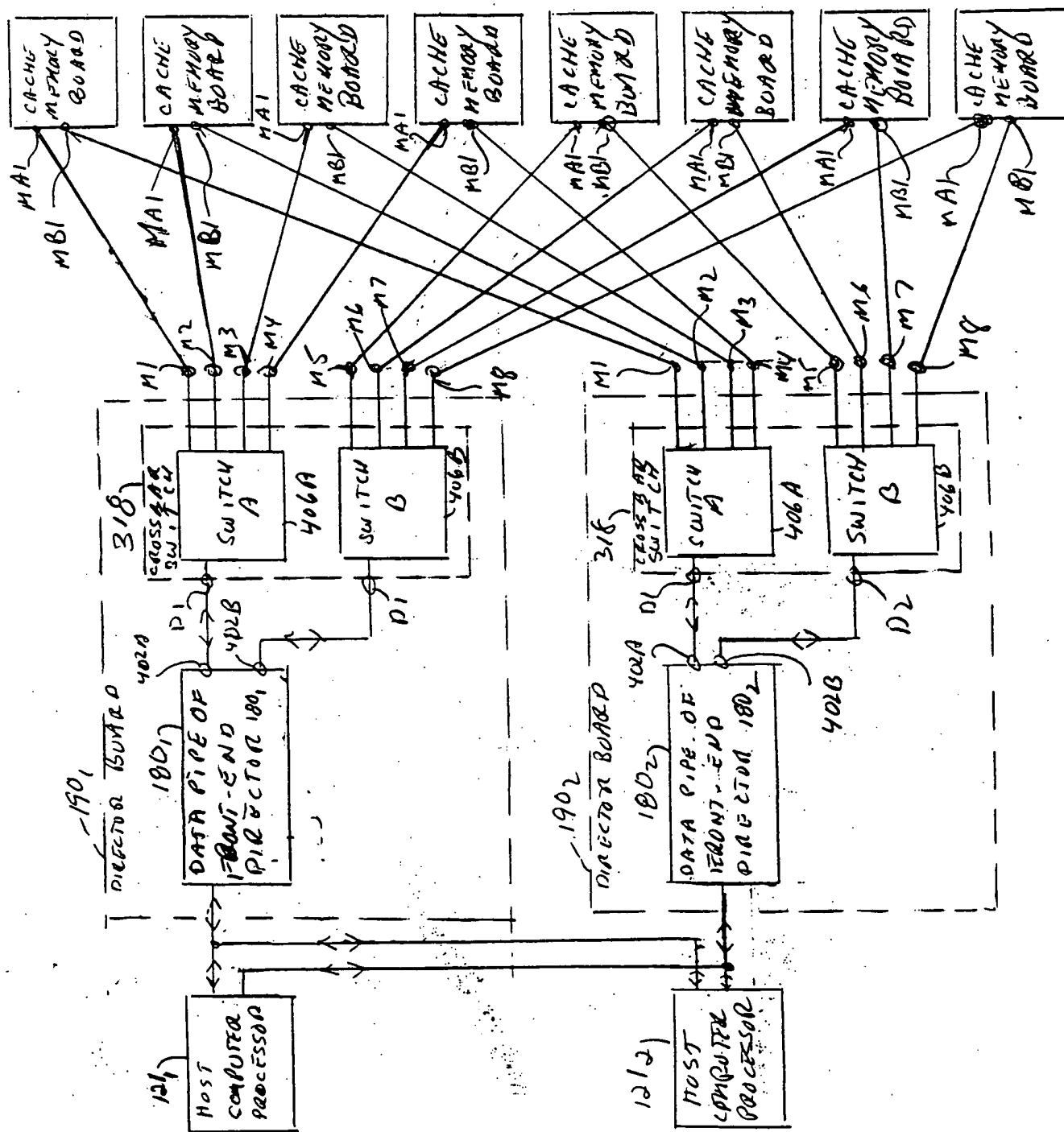


FIG. 8C

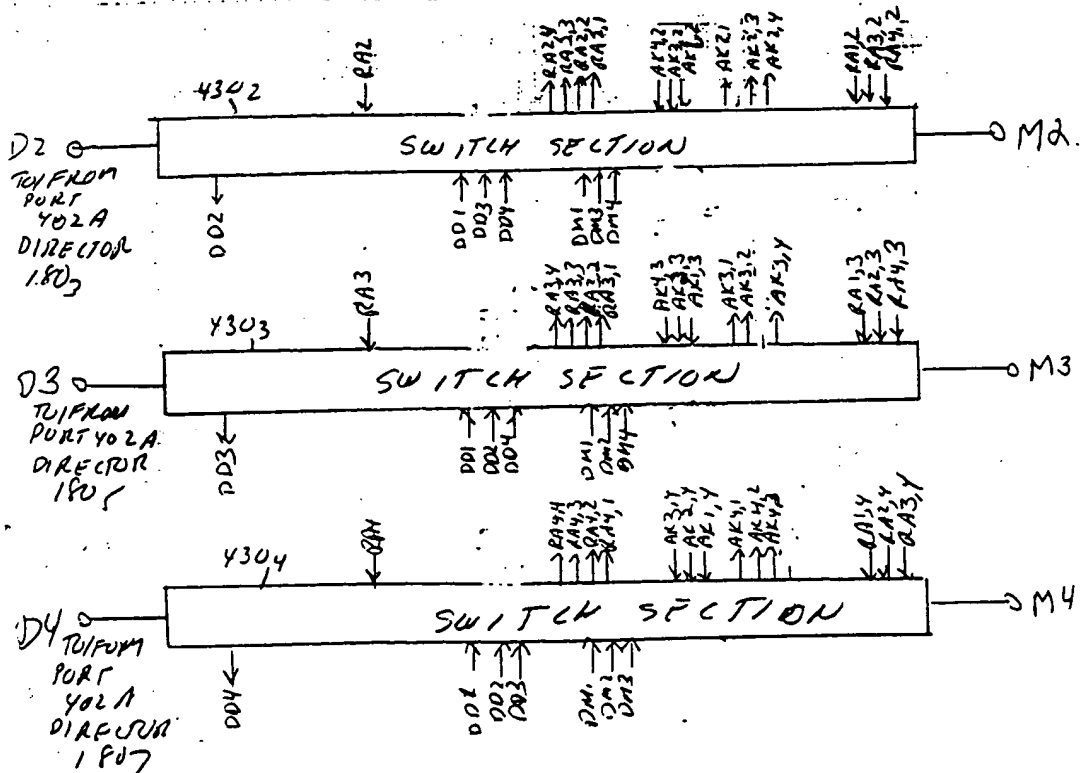
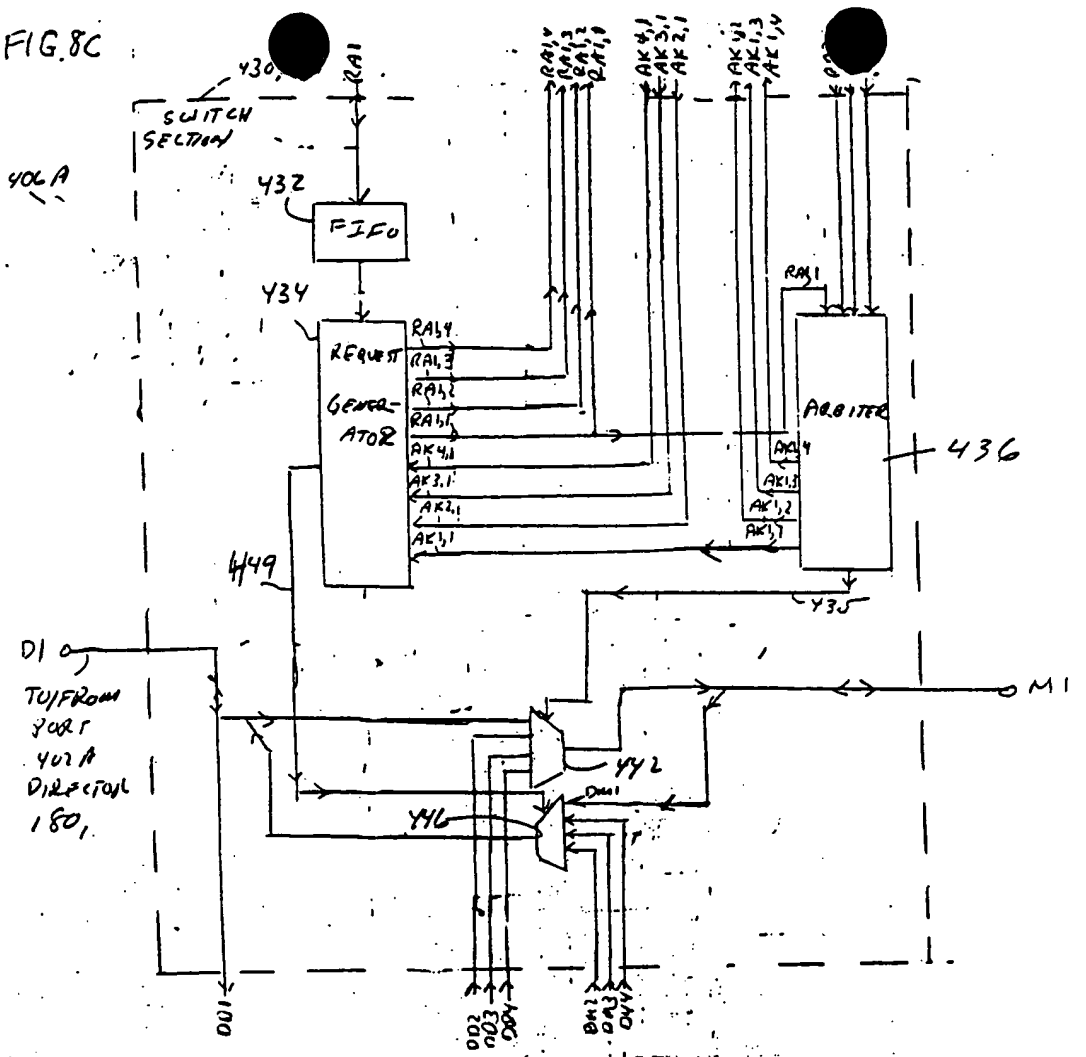
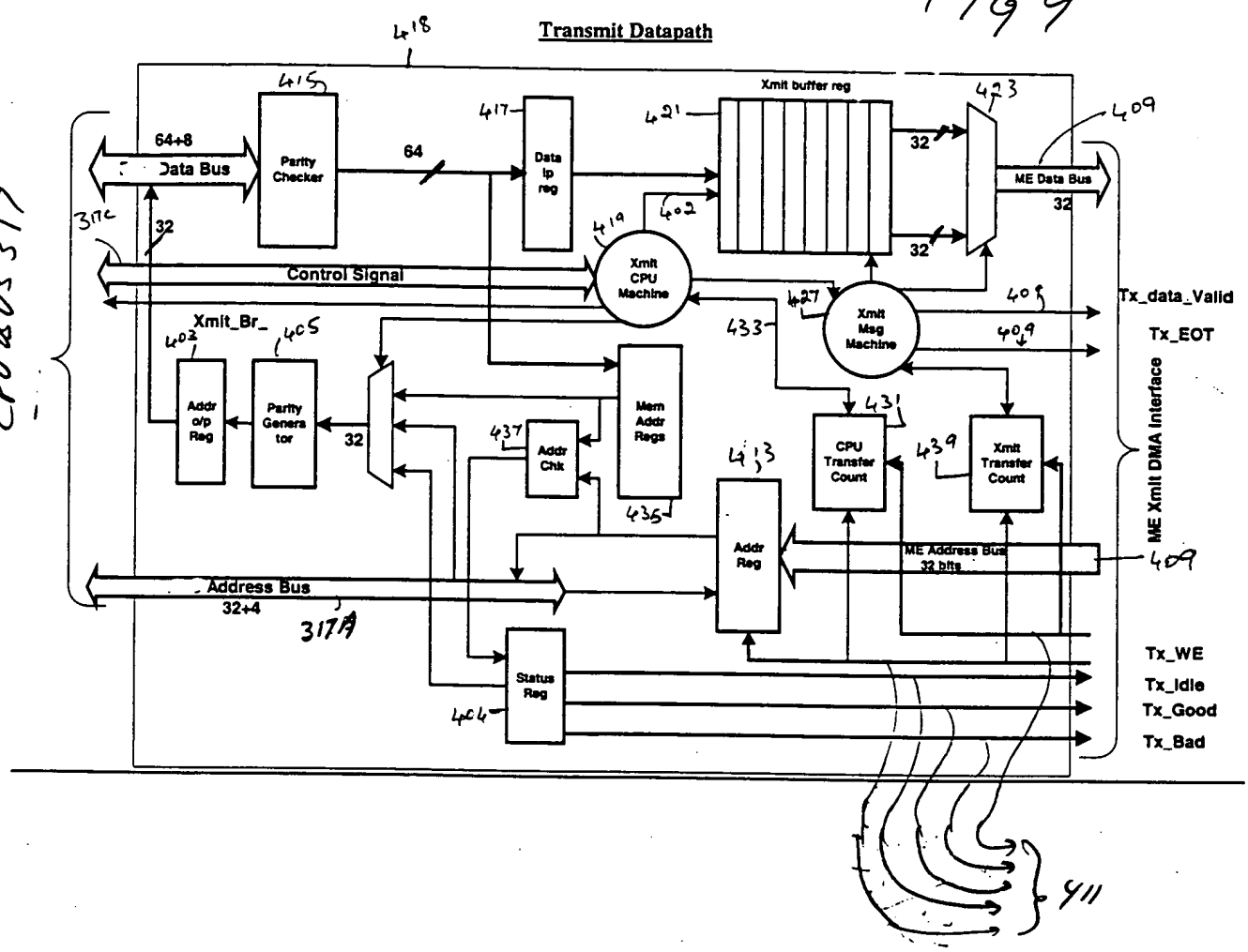


FIG 9

CPU BUS 317

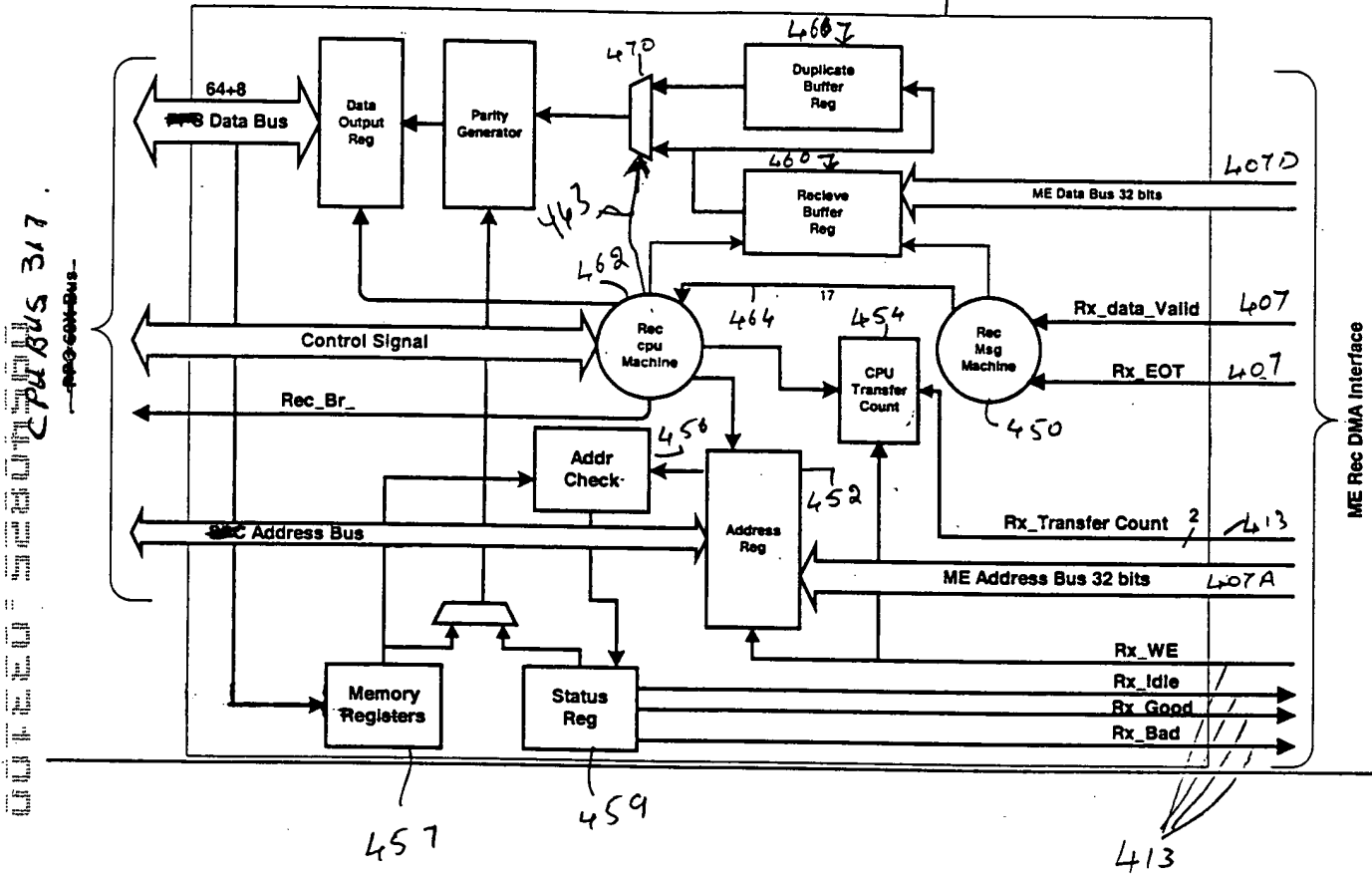
Transmit Datapath



F1610

420

Receive Datapath



Message Bus Send Operation

FIG. 11A

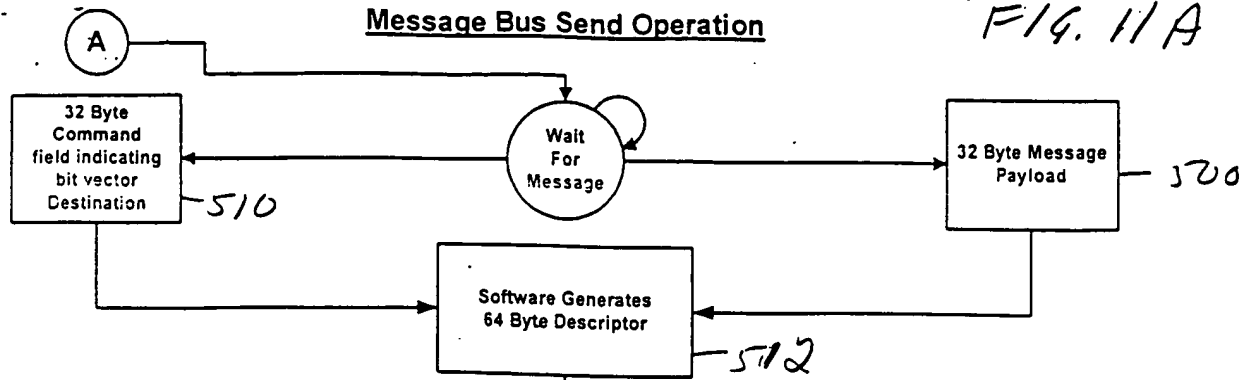
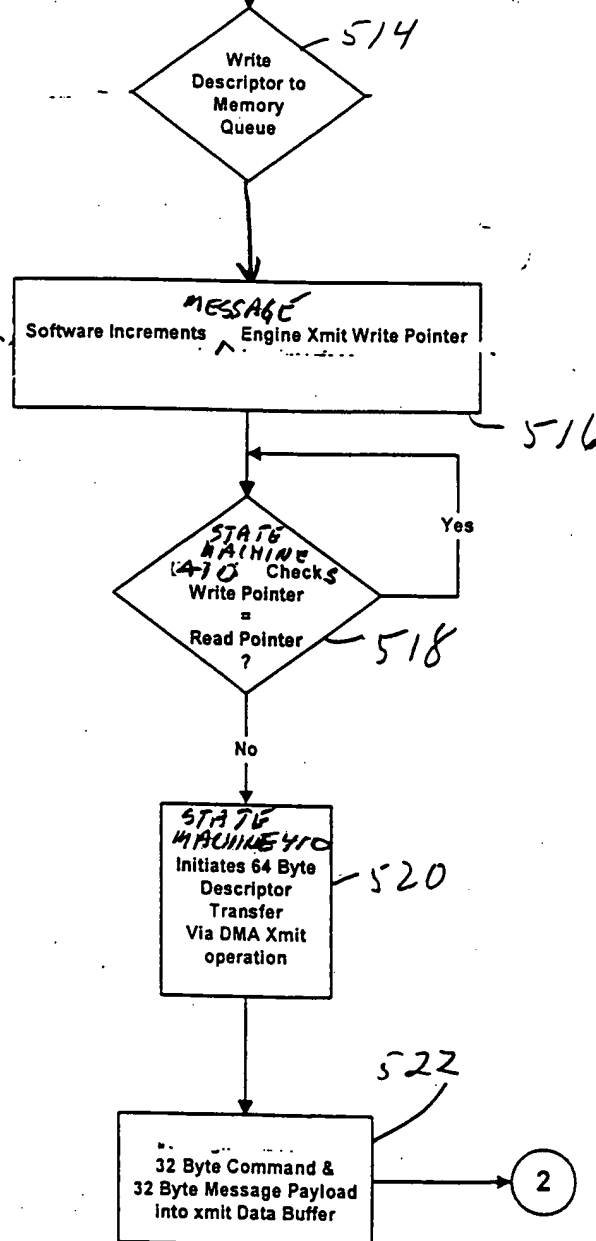


FIG 11

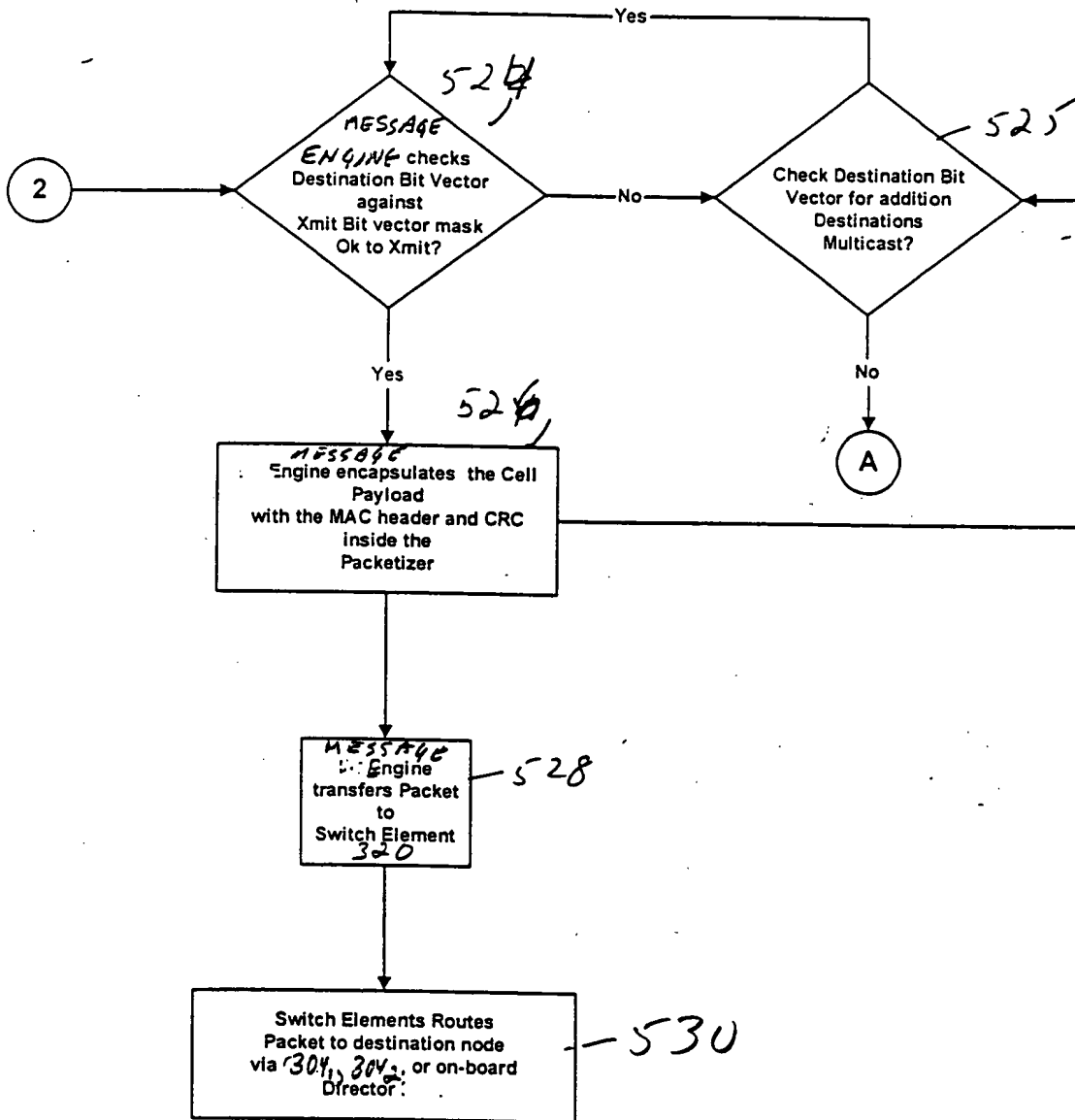
FIG 11A
FIG 11B

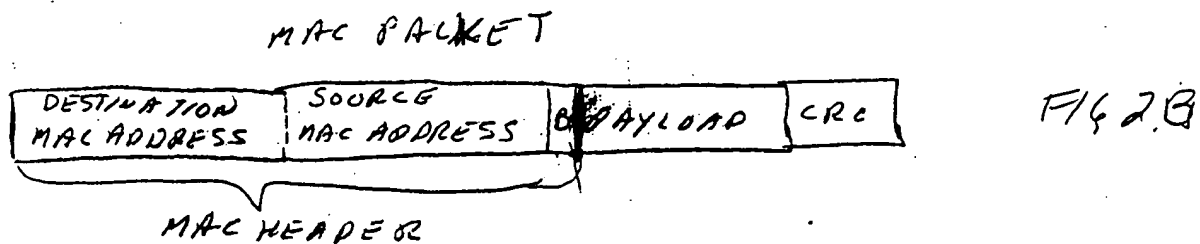
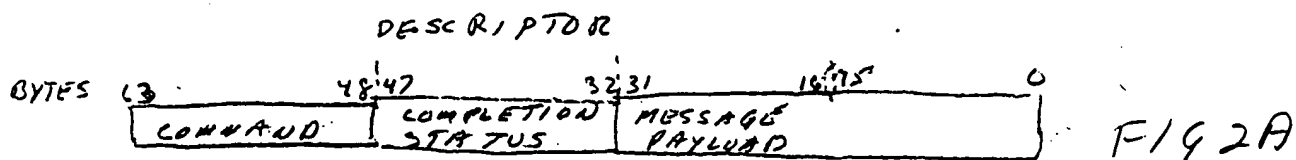
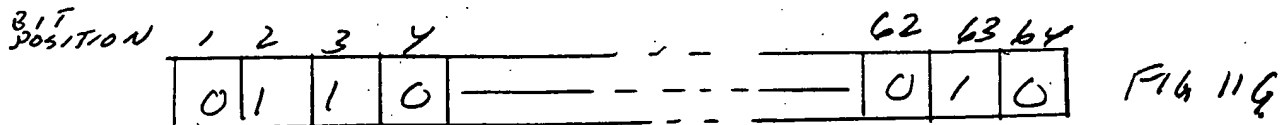
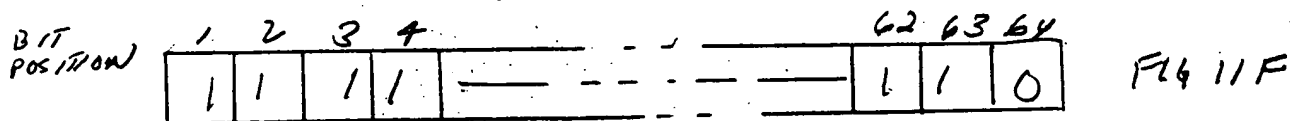
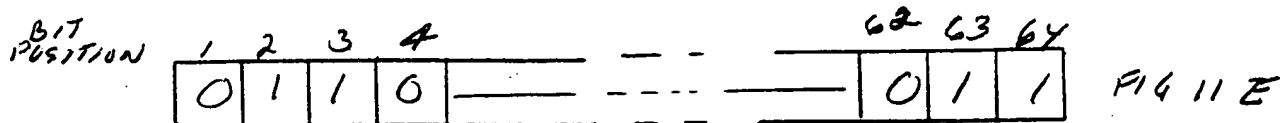
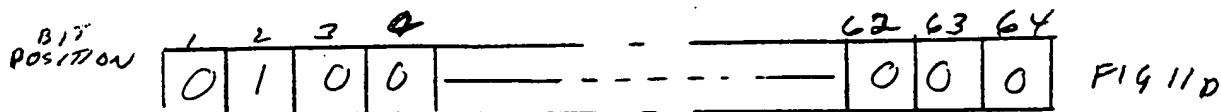
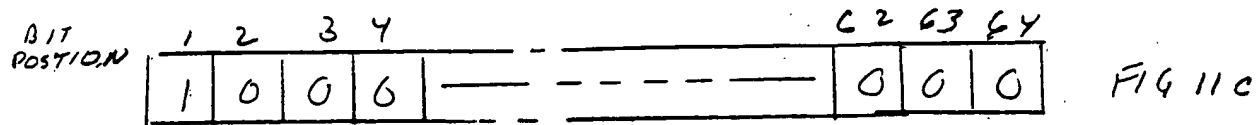


001220-0000

Message Bus Send Operation Continued

F1611B





Message Bus Receive Operation

FIG. 12A

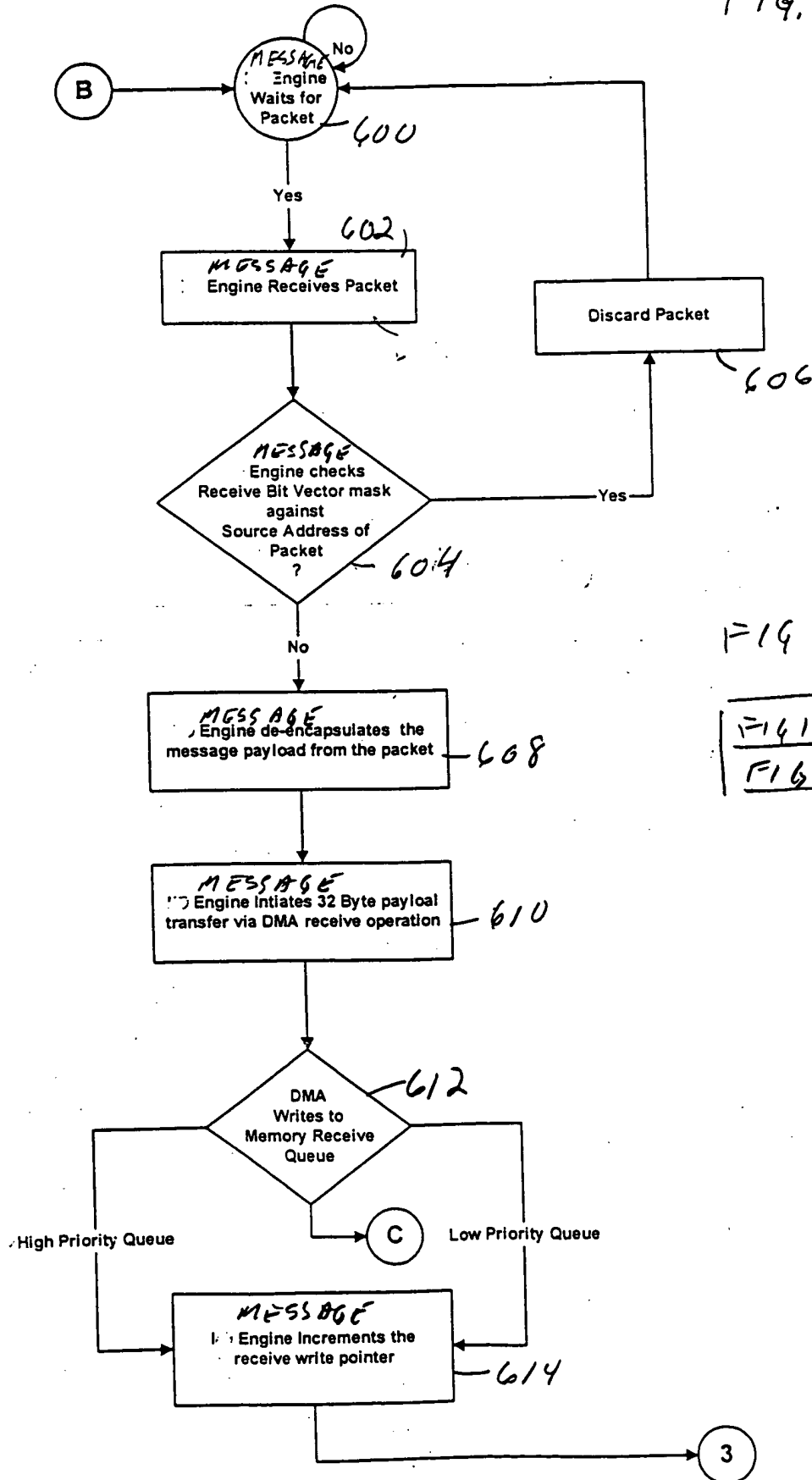
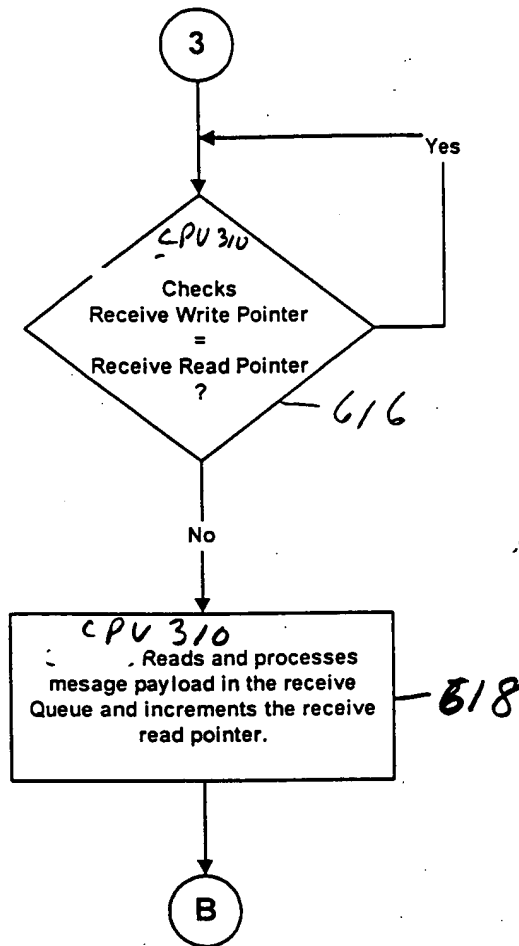


FIG 12

FIG 12A
FIG 12B

Message Bus Receive Operation Continued



F-1412B

001100-10000000

Message Bus Acknowledgement Operation

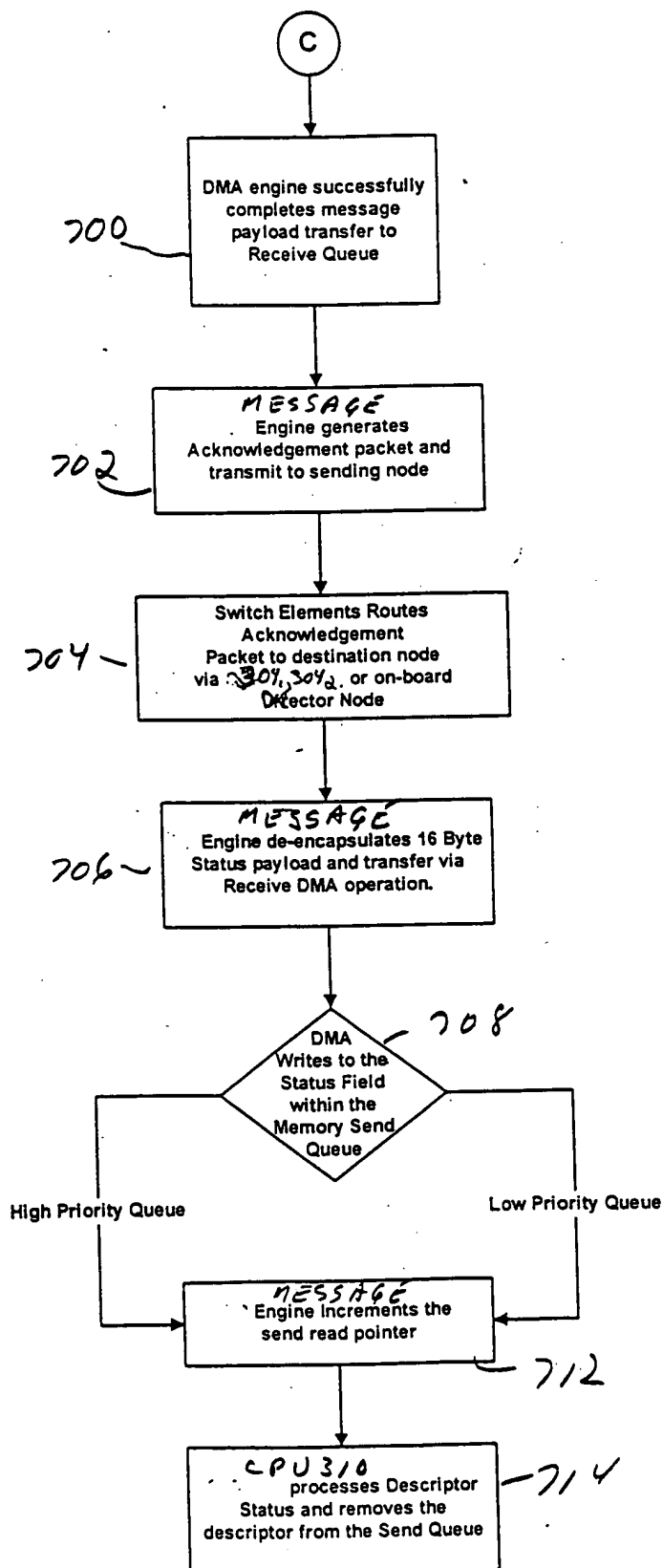
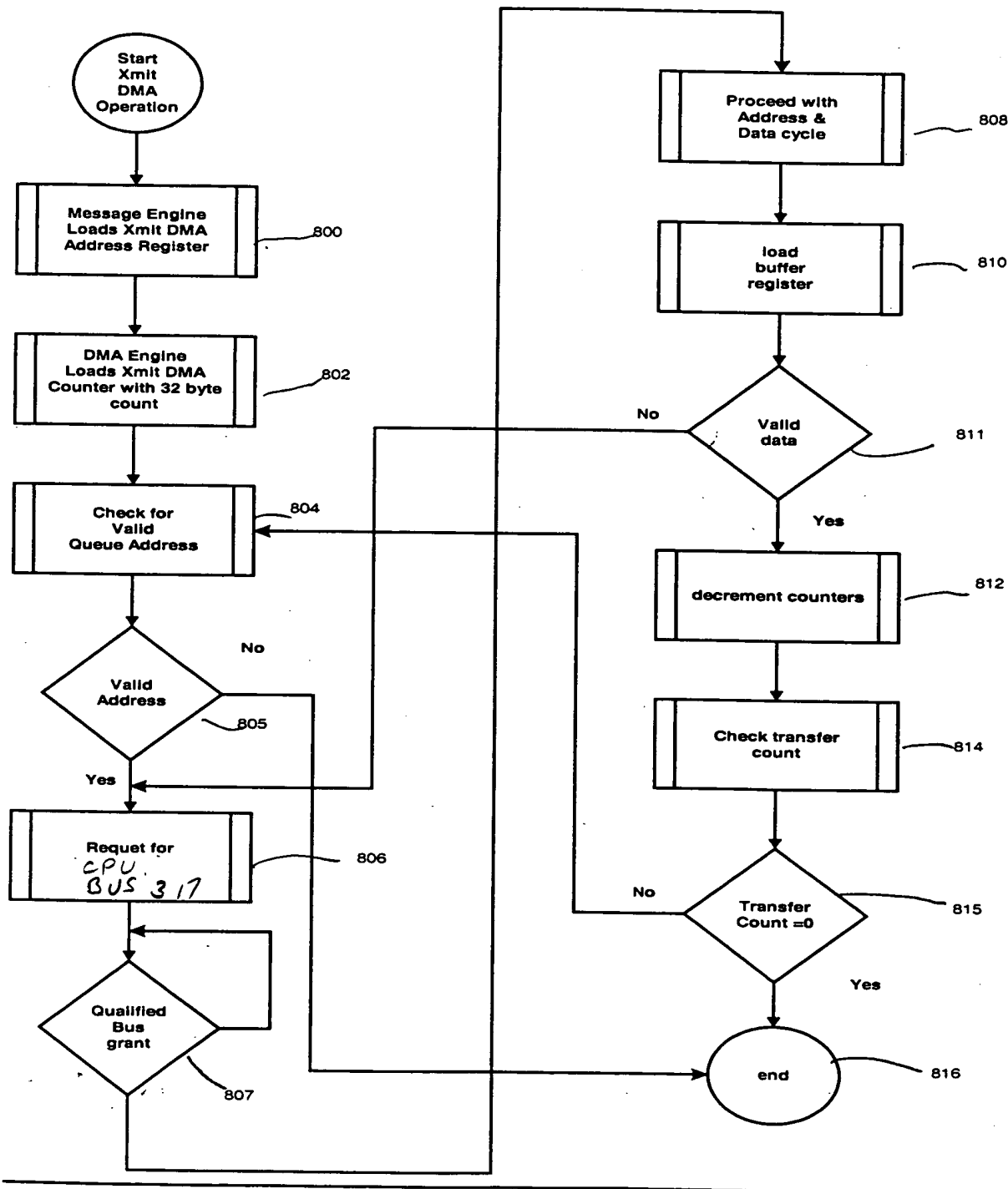


FIG 1A

Xmit CPU flow



F1414B

Xmit Msg flow

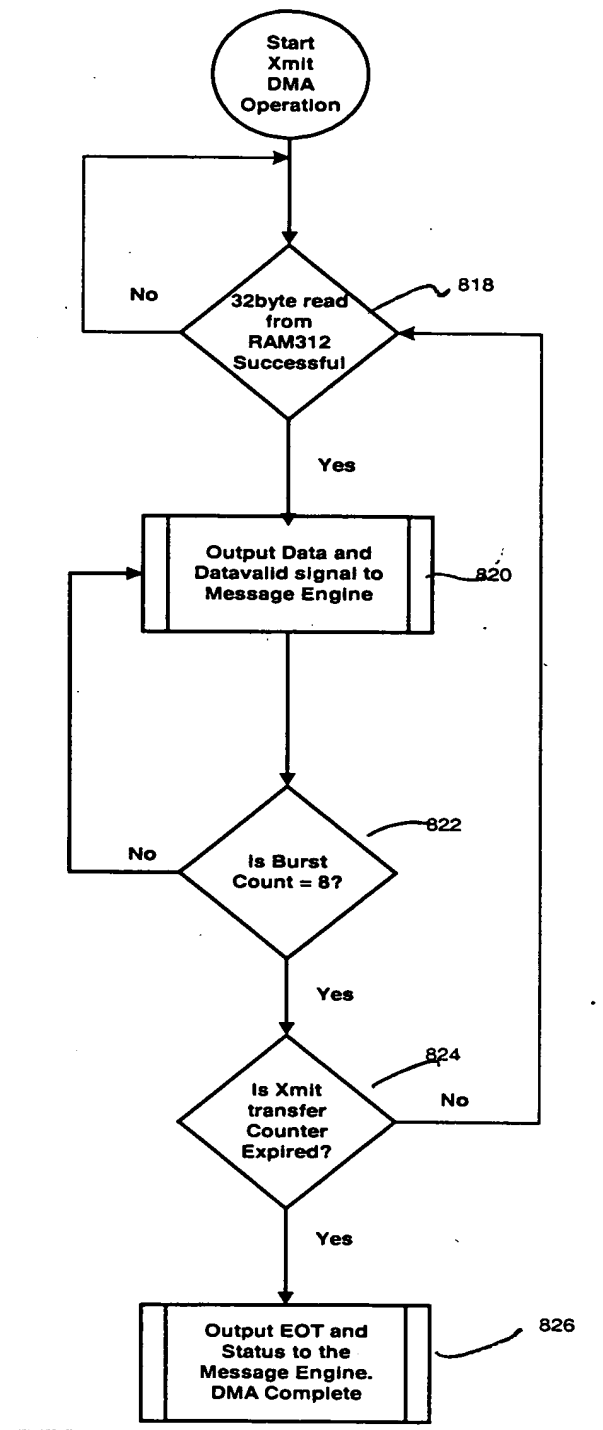
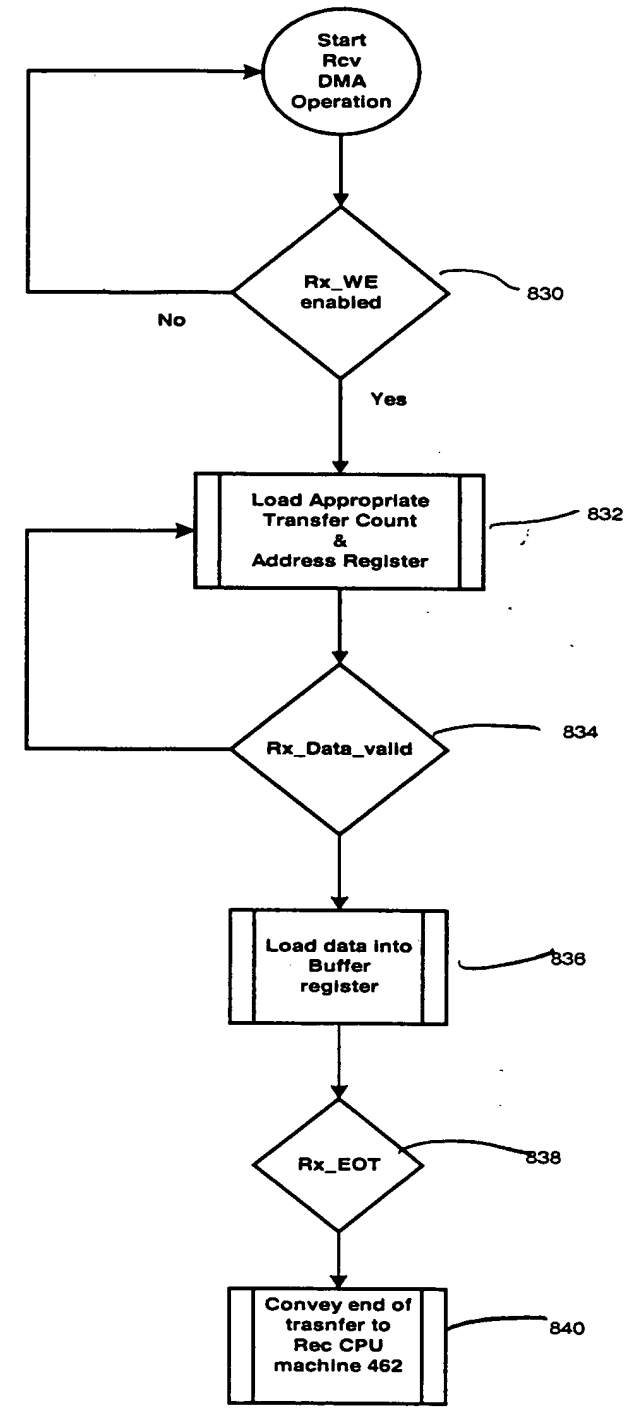


Fig 15A

Rec msg flow



F1615B

Rec cpu flow

